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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/771,279	01/26/2001	Boris A. Kurchuk	1294-00 (Kurchuk 3)	9162
7590 11/08/2004			EXAMINER	
IP Department		HEALY, BRIAN		
Schnader Harrison Segal & Lewis 36th Floor			ART UNIT	PAPER NUMBER
1600 Market Street			2883	
Philadelphia, PA 19103			DATE MAILED: 11/08/200	4

Please find below and/or attached an Office communication concerning this application or proceeding.

		rtu.
	Application No.	Applicant(s)
	09/771,279	KURCHUK, BORIS A.
Office Action Summary	Examiner	Art Unit
	Brian M. Healy	2883
The MAILING DATE of this communicati Period for Reply	on appears on the cover sheet wi	th the correspondence address
A SHORTENED STATUTORY PERIOD FOR THE MAILING DATE OF THIS COMMUNICAT - Extensions of time may be available under the provisions of 37 after SIX (6) MONTHS from the mailing date of this communica - If the period for reply specified above is less than thirty (30) day - If NO period for reply is specified above, the maximum statutory - Failure to reply within the set or extended period for reply will, be Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	FION. CFR 1.136(a). In no event, however, may a rition. Is, a reply within the statutory minimum of third, period will apply and will expire SIX (6) MON by statute, cause the application to become AE	eply be timely filed y (30) days will be considered timely. THS from the mailing date of this communication. ANDONED (35 U.S.C. § 133).
Status		
1) Responsive to communication(s) filed or	۱ <u></u> .	
2a) This action is FINAL . 2b) ∑	☐ This action is non-final.	
3) Since this application is in condition for a	allowance except for formal matt	ers, prosecution as to the merits is
closed in accordance with the practice u	nder <i>Ex parte Quayle</i> , 1935 C.D	. 11, 453 O.G. 213.
Disposition of Claims		
4)⊠ Claim(s) <u>1-18</u> is/are pending in the appli	cation.	
4a) Of the above claim(s) is/are w	ithdrawn from consideration.	
5)⊠ Claim(s) <u>17 and 18</u> is/are allowed.		
6) Claim(s) <u>1,3-5,7,8 and 13-16</u> is/are reject	cted.	
7) Claim(s) 2,6 and 9-12 is/are objected to.		
8) Claim(s) are subject to restriction	and/or election requirement.	
Application Papers		
9)☐ The specification is objected to by the Ex	aminer.	
10)⊠ The drawing(s) filed on 26 January 2001	is/are: a)⊠ accepted or b)□ o	bjected to by the Examiner.
Applicant may not request that any objection	to the drawing(s) be held in abeyar	ice. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the	correction is required if the drawing	(s) is objected to. See 37 CFR 1.121(d).
11) The oath or declaration is objected to by	the Examiner. Note the attached	Office Action or form PTO-152.
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for f	oreign priority under 35 U.S.C. §	119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of: 1. ☐ Certified copies of the priority doc	umanta haya hasa usasiyad	
 Certified copies of the priority doc Certified copies of the priority doc 		anlication No.
3. Copies of the certified copies of the		· ·
application from the International	· · ·	received in this National Stage
* See the attached detailed Office action for	` ' ''	Brian Healy
Attachment(s)		Primary Examinor
1) Notice of References Cited (PTO-892)	4) Interview S	Summary (PTO-413)
2) Notice of Draftsperson's Patent Drawing Review (PTO-93) Information Disclosure Statement(s) (PTO-1449 or PTO		s)/Mail Date nformal Patent Application (PTO-152)
Paper No(s)/Mail Date <u>10282004</u> .	6) Other:	·

U.S. Patent and Trademark Office PTOL-326 (Rev. 1-04) Application/Control Number: 09/771,279

Art Unit: 2883

DETAILED ACTION

Allowable Subject Matter

Claims 2, 6, and 9-12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. The closest prior art of record, Varasi et. al. '390 does not teach or suggest the limitations of claim 1 along with having a linear circuit comprises a photodiode electrically connected to an amplifier and the tunable filter electrically connected to the amplifier wherein the amplifier receives an output signal from the photodiode and provides an amplified signal to the tunable filter with the control circuit comprises a digitally controlled variable resistor. Also none of the references of record teaches the limitations of claim 1 with the tunable filter comprises one or more varactors and the control circuit comprises a tunable DC voltage source and control circuit comprises a reference voltage source, one or more comparators, a first voltage divider, a second voltage divider wherein one or more comparators compare a signal from the sensor(s) with a reference voltage output by the first voltage divider and modifies the reference voltage to substantially equal a predetermined optimum control voltage which is input to the second voltage divider and the second voltage divider outputs a signal to the tunable filter and thus modifying the linear circuit transfer function according to the conditions determined by the sensors. Finally, none of the references of record teaches or suggests the limitations of claim 1 that further comprises a second filter connected between the tunable filter and the control circuit.

Application/Control Number: 09/771,279

Art Unit: 2883

Claims 17 and 18 are allowable over the teachings of Varasi et. al., '390 (discussed below) because neither Varasi et. al. and any of the other references of record teaches or suggests a method of adjusting an optoelectronic signal in a linear circuit (see claims 17 and 18 for the specific listed steps.)

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1,3-5,7-8 and 13-16 are rejected under 35 U.S.C. 102(b) as being anticipated by Varasi et. al., U.S.P. No. 5,493,390.

Varasi et. al., U.S.P. No. 5,493,390 teaches (Figs.1-12) an integrated optoelectronic optical sensor/receiver comprising: a plurality of sensors 3 that sense parameters such as temperature and strain with a linear circuit 7 that includes a wavelength bandwidth tunable filter and an optical detector 9 (It is inherent that this detector can be an avalanche photodiode as is considered standard in optical detector circuits) that is routed to a control circuit 33 (control circuit includes memory) that generates a control signal that is input to the tunable filter (note the tunable filter appears to be a three-pole filter) and the control signal adjusts the linear circuits transfer function based on the control signal and uses a low pass filter 45 for noise filtration, which clearly, fully meets Applicant's claimed limitations.

Art Unit: 2883

A copy of PTO-1449 will be included in this office action.

The following references are also cited by the Examiner as being pertinent prior art: Sonnichsen et. al., U.S.P. No. 6,098,022 (Figs.1-7), Hung, U.S.P. No. 6,333,941 (Figs.1-5), Sawyers et. al., U.S.P. No. 5,408,863 (Figs.1-3) and May, U.S.2004/0119981A1 (Figs.1-7).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian M. Healy whose telephone number is (571)272-2347. The examiner can normally be reached on Compressed Schedule Tues-Thurs. 7AM-4;30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Frank Font can be reached on (571)272-2415. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Application/Control Number: 09/771,279

Art Unit: 2883

Page 5

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Brian M. Healy Primary Examiner Art Unit 2883

Brian Healy Primary Examiner